

## Amendments to the Specification

Please replace the text on page 12, line 3, to page 13, line 1, with the following:

### 5-1 List of Figures

Other features and advantages of the invention shall appear more clearly from the following description of a preferred embodiment of the invention given as a simple non-restrictive illustration and from the appended drawings, of which:

- Figure 1 gives a general and simplified illustration of the method of modulation of the invention (step j) used to generate M samples;
- Figure 2 illustrates the gradual construction of the M samples where  $L = 4$ ; for the Iota waveform;
- Figures 2a, 2b and 2c are closer views of portions of Fig. 2, broken out as indicated by the lines on Fig. 2.
- Figure 3 gives a more detailed illustration of the working of the method of modulation of the invention for the instances ~~instants~~  $j-1$  to  $j+2$ ;
- Figures 3a, 3b, 3c and 3d are closer views of portions of Fig. 3, broken out as indicated by the lines on Fig. 3.
- Figure 4 specifies the initiation of the procedure of modulation of Figure 3, where  $L = 4$ ;
- Figures 4a and 4b are closer views of portions of Fig. 4, broken out as indicated by the lines on Fig. 4.
- Figure 5 is a schematic diagram of a complex IFFT circuit known per se;
- Figures 6A to 6C illustrate the optimized architectures implementing FIFO systems and respectively using a single multiplier (Figure 6A), L multipliers (Figure 6B) or 2L multipliers (Figure 6C);
- Figure 7 shows an optimized embodiment of the reverse FFT using a real input FFT;
- Figure 8 illustrates the working of the demodulation method of the invention when  $L = 4$ ;

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- Figures 8a, 8b and 8c are closer views of portions of Fig. 8, broken out as indicated by the lines on Fig. 8.

- Figure 9 shows the general case of demodulation deduced directly from Figure 8;
- Figure 10 illustrates a corresponding demodulator architecture;
- Figures 11 to 12 show two modes of implementation of the reception filtering in the case of a FIFO structure respectively using L and 2L multipliers.

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